IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with underlining and deleted text with ~~strikethrough~~.

Please REPLACE paragraph [0082], with the following paragraph:

[0082]   For the implementation based on the specification (IUT : Implementation Under Test), it is necessary to verify that each operation is correctly implemented (conformance testing). For example, the verification of the transition $s_2 \rightarrow s_5$ is performed as follows. The input/output action corresponding to the transition $s_2 \rightarrow s_5$ is a/w.

1. The IUT makes a transition from the initial state to state s_2.

2. Input a is presented and it is verified that output w is obtained.

3. It is verified that the IUT made a transition to state s_5. If we can verify that IUT is in state s_5, it is decided that the transition is correctly implemented.